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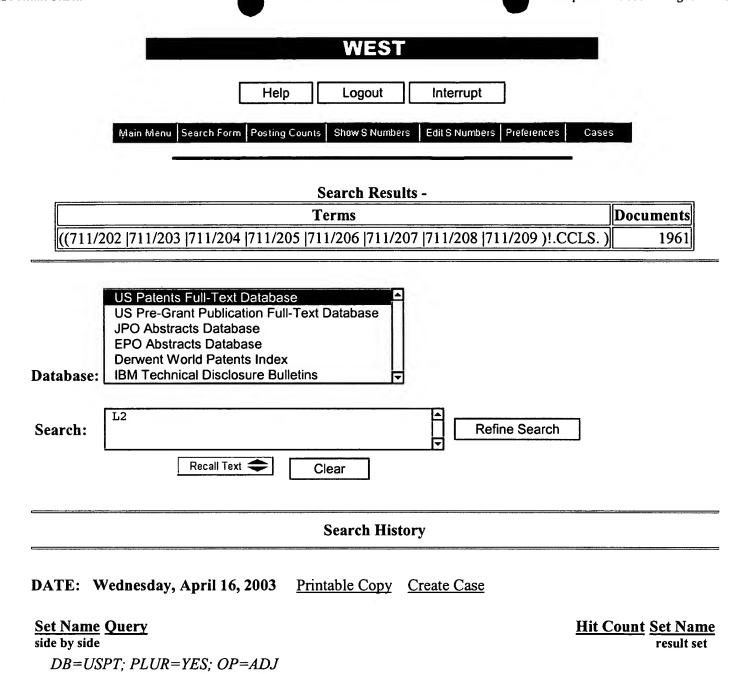
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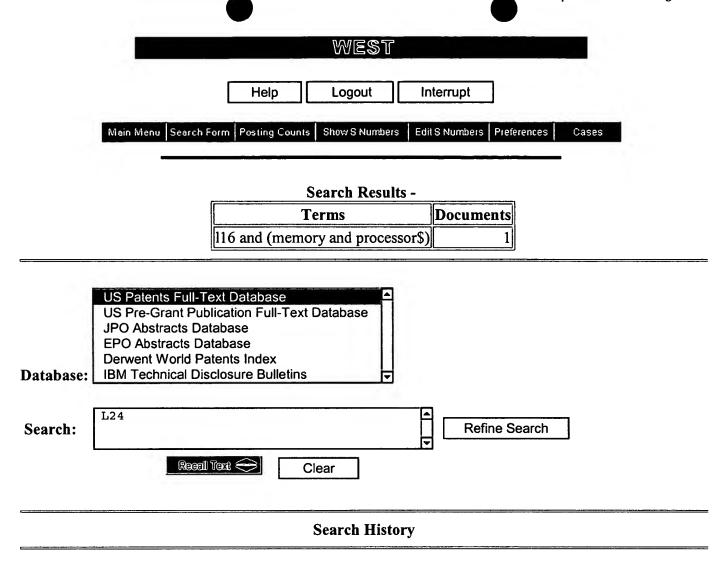
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<u>L24</u>	116 and (memory and processor\$)	1	<u>L24</u>	
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<u>L17</u>	L16 and TLB and (modif\$ or updat\$ or alter\$ or chang\$)	1	<u>L17</u>	
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